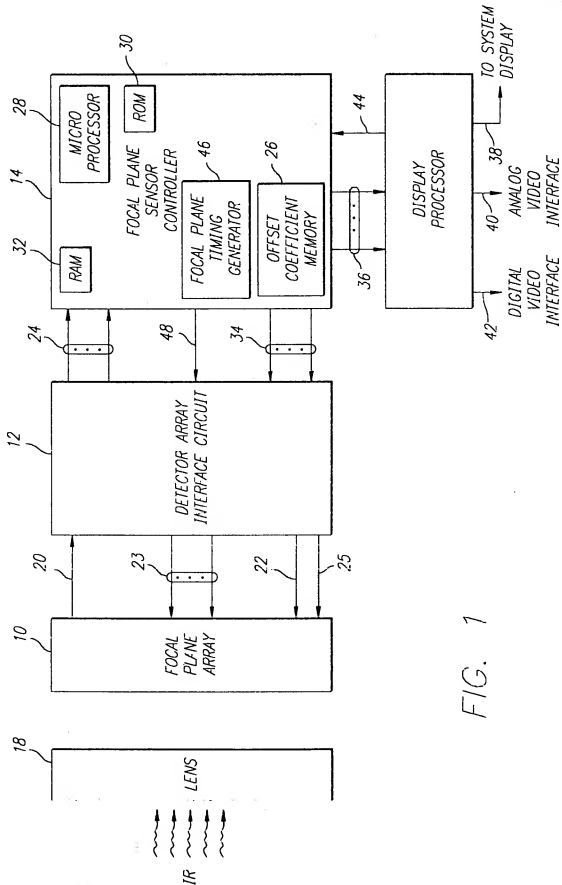


08/ 712891



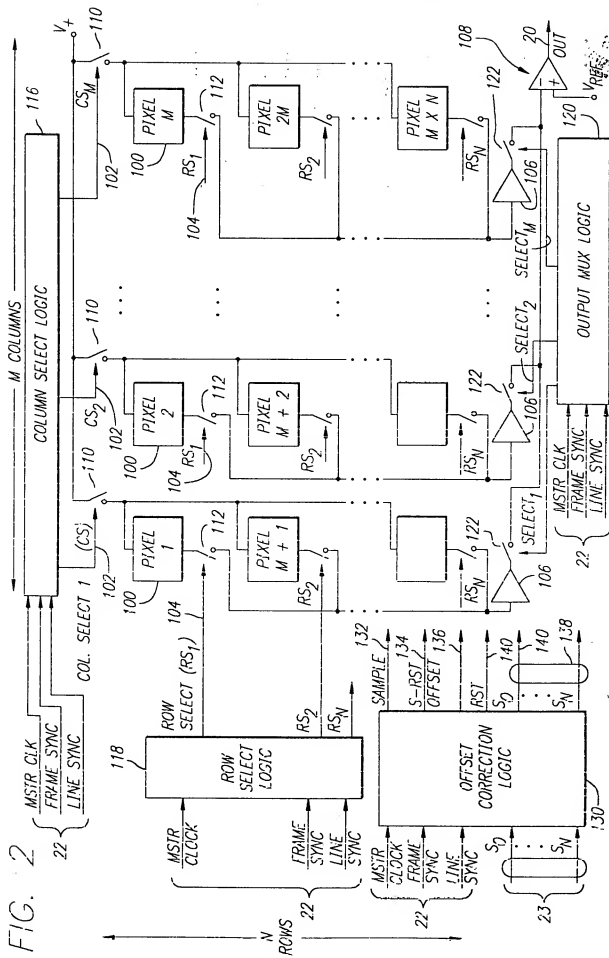




FIG. 3A

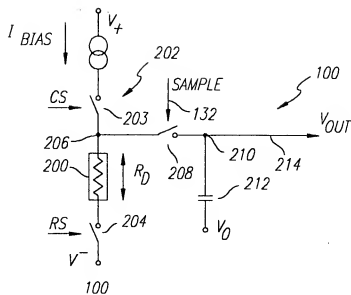


FIG. 3B

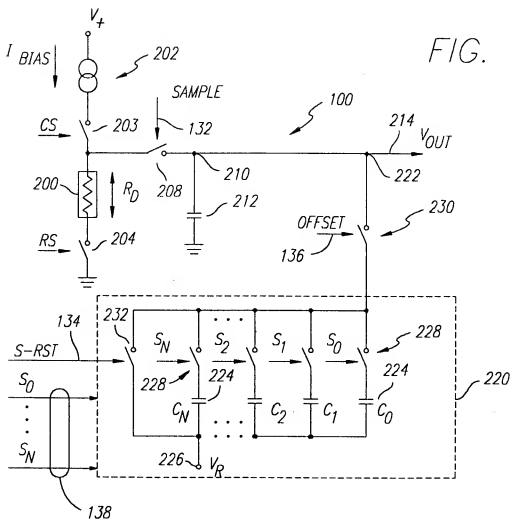
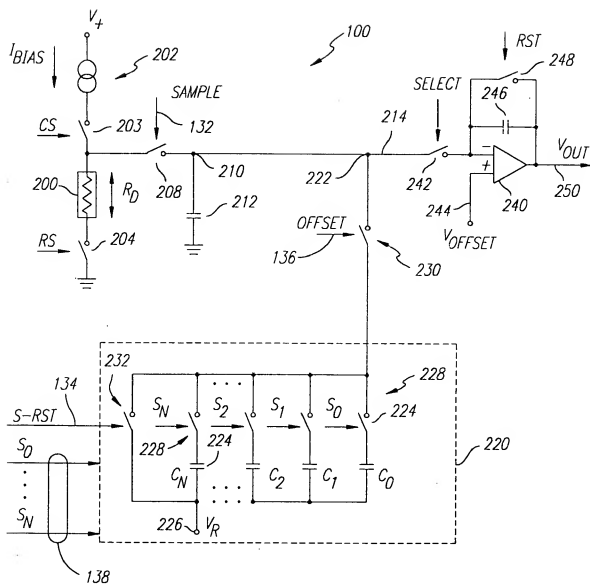
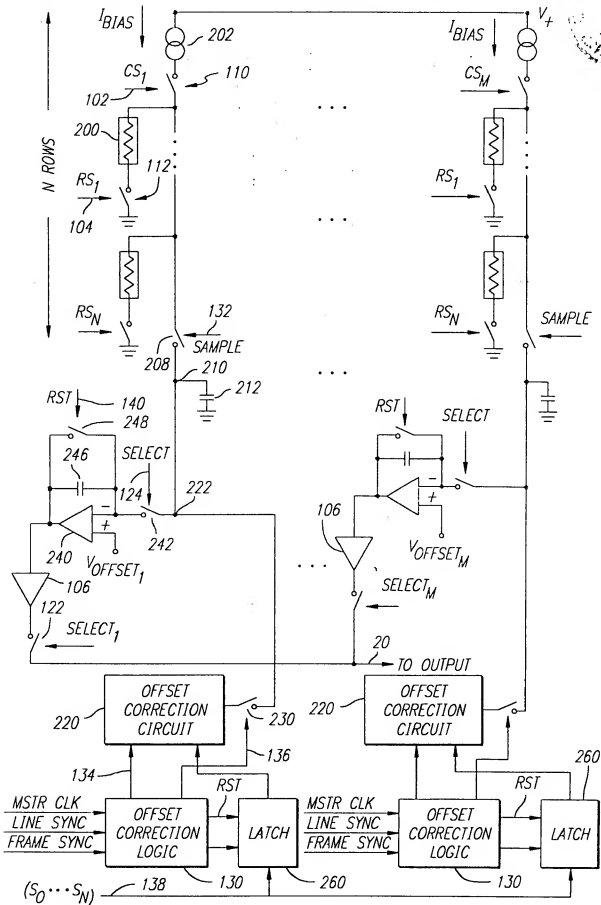




FIG. 3C



M COLUMNS



RS

55

CS

55

SAMPLE

55

OFFSET

55

SELECT

55

S-RST

55

 S_0

55

 S_1

55

 S_2

55

 S_3

55

RSI

55

FIG. 6A

SELECT
MSTR CLOCK

FIG. 6B

LINE SYNC

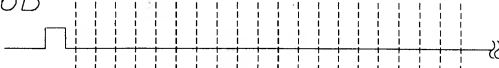


FIG. 6C

S0



FIG. 6D

S1



FIG. 6E

S2



FIG. 6F

S3

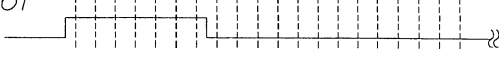


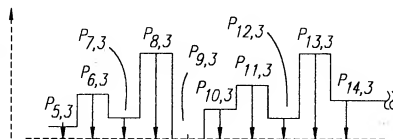
FIG. 6G

BINARY
OFFSET
COEFFICIENT

0011	1011	1111	0011	1010	1000	1000	1100	0100	1100
1011	1111	0011	0001	1010	0000	1100	0100	0100	1100

TIME

FIG. 7

SAMPLED
VOLTAGE (V_D) V_D 

TIME

FIG. 8

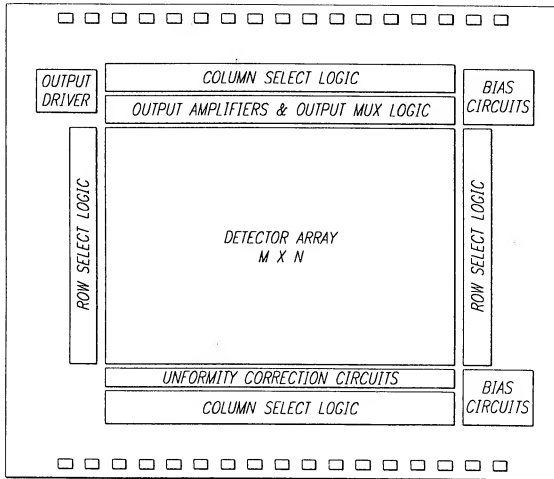


FIG. 9

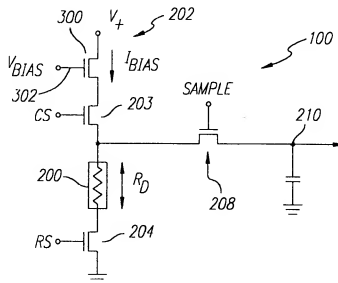


FIG. 10

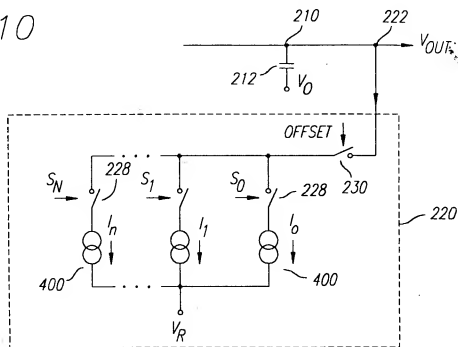


FIG. 11

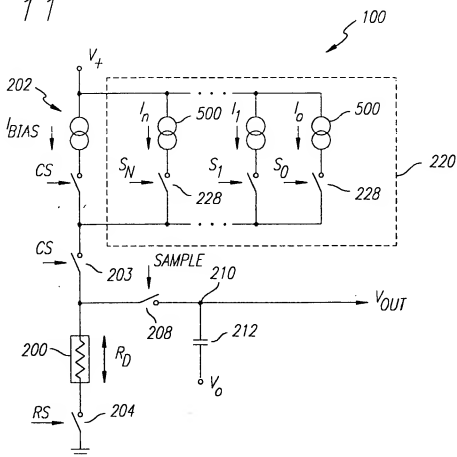




FIG. 12

